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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	10/050,348 January 15, 2002 Gurtej S. Sandhu et al.
Filing Date	Japuan 15, 2002
Inventor	Gurtai S. Sandhu et al
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	2042
Examiner	L. Schillinger
Attorney Docket No.	MI22-1898
Title: Transistor Structures	WIIZZ-1898

ART UNIT 2813 Commissioner for Patents Alexandria, VA 22313

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

1. Certificate of Facsimile Transmission.

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JAN 1 3 2004

Inventor:

Gurtej S. Sandhu et al.

Title:

Transistor Structures

Assignee:

Micron Technology, Inc.

Serial No.: 10/050,348

Filed:

January 15, 2002

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the references listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of an RCE application OR before the mailing of a first office action after the RCE, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Citation of these references is respectfully requested.

Respectfully submitted.

Attorney: _

David & Latwesen, Ph.D.

Reg. No. 38,533

Wells St. John P.S.

Sheet 1 of 1

Form PTC-1449 U.S. DEPARTMI PATENT AND TO				DEPARTMEN	NT OF COMMER	ю.		SERIAL NO. 10/050.348							
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)								APPLICANT Guriej S. Sandbu et	APPLICANT						
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U.S. PATENT DOCUMENTS															
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